

# Cray SHMEM on XT3

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#### **Outline**

- Introduction to SHMEM
- 2. Cray SHMEM implementation on XT3
- 3. Cray SHMEM 1.0 Release on XT3
- 4. Future Work
- 5. References



#### 1. Introduction to SHMEM

- Programming Model
  - Memory is private to each process:
    Remotely accessible, not shared
  - SHMEM is one-sided message passing model:
    Put and get operations



## Introduction(cont.)

- Programming Model (cont.)
  - SHMEM is SPMD programming model
    - Processes run in parallel from launch to termination
    - No processes can be added or removed
    - All processes execute same application asynchronously
    - Synchronization for data exchanges
    - SHMEM application can be part of MPMD type MPI job

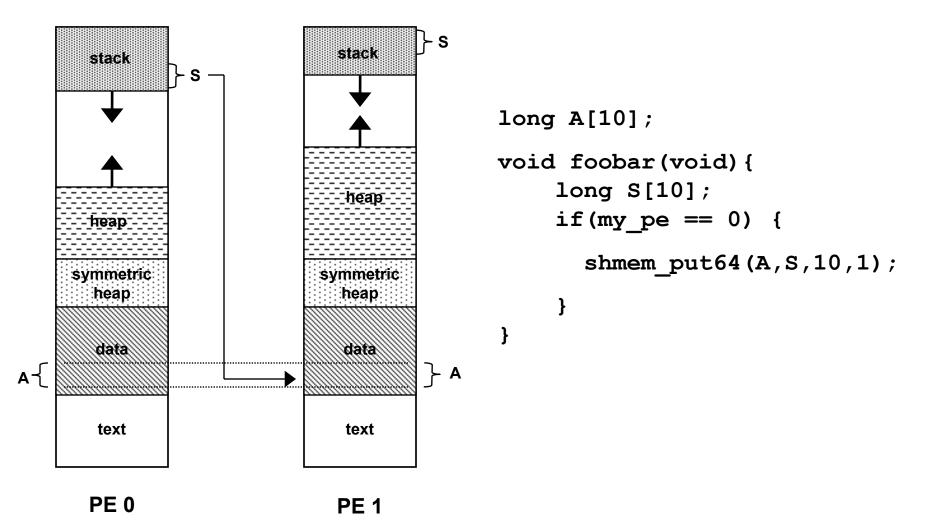


## Introduction(cont.)

- Symmetric Data Objects
  - Primary concept in SHMEM
  - Virtual addresses of symmetric data object on different processes have definite, known relationship
    - Access remote symmetric data objects by using address of corresponding local data object
  - C: global, static or shmalloc'd data
  - Fortran: common block, SAVE attribute or shpalloc'd data



#### Introduction (cont.)





## Introduction(cont.)

#### Goal

Deliver best possible communication performance by minimizing overhead associated with data transfer

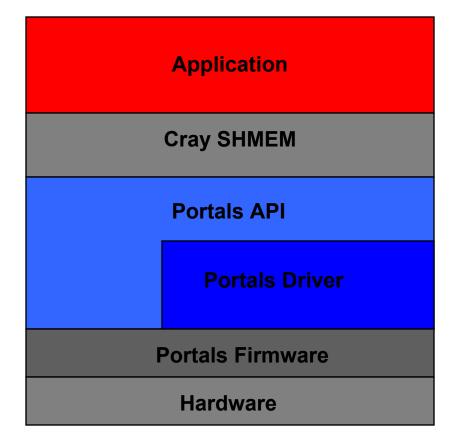


# 2. Cray SHMEM Implementation on XT3

- XT3 uses Portals Networking Protocol
  - One-sided RMA protocol
  - Guarantees reliable, ordered message delivery between pairs of processes
  - Connection-less
  - Designed specifically for scalability
- Cray SHMEM layered on top of Portals 3.3



#### Cray SHMEM on XT3 (cont.)





# Cray SHMEM on XT3 (cont.)

- Portals resources
  - Memory Descriptor (MD) identifies a memory region to be used in operation
  - Event Queue (EQ) used to record information about operation
- SHMEM start-up
  - Set up Portals resources
    - MDs to describe four memory regions
    - EQ to monitor transfer completions



## Cray SHMEM on XT3 (cont.)

- SHMEM data transfer
  - Source and target addresses determine which MDs and EQs to supply to Portals call
  - Execute Portals put or get command
  - Monitor EQ for completion event if necessary
  - Persistent Portals resources => low overhead on transmit path



#### 3. Cray SHMEM 1.0 Release

- Functionality Supported
  - Initialization and Clean up
    - shmem\_init or start\_pes
    - shmem\_finalize
  - Queries
    - shmem\_my\_pe, shmem\_n\_pes
  - Puts and Gets
    - shmem\_xxx\_{put,get} (generic & different types)
    - shmem {put,get}xxx (different bit counts)
    - shmem\_{put,get}mem



- Functionality Supported (cont.)
  - Synchronization
    - shmem\_fence
    - shmem\_quiet
    - shmem\_barrier\_all
    - shmem\_barrier
  - Wait
    - shmem\_xxx\_wait (generic & different integer types)
    - shmem\_xxx\_wait\_until (generic & different integer types)



- Functionality Supported (cont.)
  - Broadcast
    - shmem\_broadcastxxx (generic & different bit counts)
  - Reductions
    - shmem\_xxx\_yyy\_to\_all for operations sum, prod, max, min, and, or, xor (different types)
    - Currently supported on all PEs only



- Functionality Supported (cont.)
  - Events
    - shmem\_{clear,set,test,wait}\_event
  - Strided Puts and Gets
    - shmem\_xxx\_i{put,get} (generic & different types)
    - shmem i{put,get}xxx (different bit counts)



- Functionality Supported (cont.)
  - Symmetric Heap management
    - shmalloc
    - shfree
    - shrealloc
  - Fortran Interface
    - Functions corresponding to C interface
    - include 'mpp/shmem.fh'



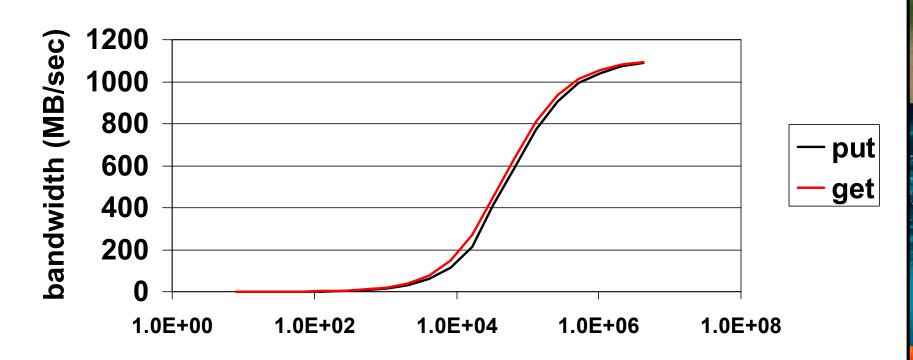
- Preliminary Performance Data
  - Simple SHMEM get/put operations map well onto XT3 architecture
  - Advanced SHMEM operations do <u>not</u> map well onto XT3 architecture
  - Portals not tuned yet, e.g. no OS-bypass



- Preliminary Performance Data (cont.)
  - MPI latency
    25 usec for 8 bytes
  - SHMEM put latency
    22 usec for 8 bytes



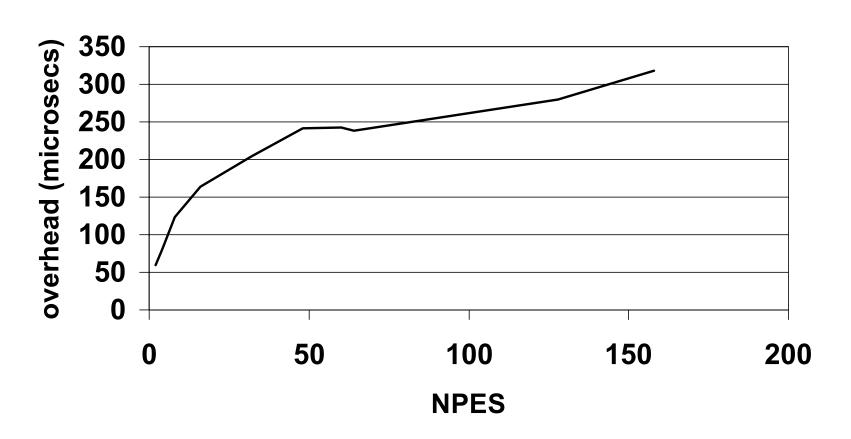
shmem\_put/get performance



transfer size (bytes)



shmem\_barrier\_all overhead





#### 4. Future Work

- Add Further Functionality
  - Non-blocking put and get operations
  - Atomic operations, depends on Portals work
  - Locking functions
  - Single element put and get operations
  - Your input is welcome
  - "USE SHMEM"?
- Performance Tuning
  - Strided put and get operations
  - Collectives



#### 5. References

- The Portals 3.3 Message Passing Interface R. Brightwell, R. Riesen, 2003
- Algorithms for Scalable Synchronization on Shared-Memory Multiprocessors
   J. Mellor-Crummey, M. Scott ACM Trans. On Computer Systems, 1991
- Networking Track
  CUG 2005