

# The time is now. Unleash your CPU cores with Intel<sup>®</sup> SSDs.

Andrey Kudryavtsev SSD Solution Architect, Intel Corporation

### Memory and Storage Hierarchy

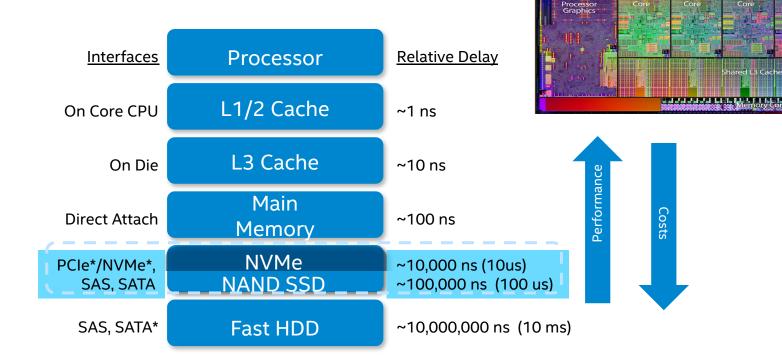


System Agent & Memory

Controller-DML Dist & Mise.

Core

Core



### NVM Solutions are bringing storage closer to the processor

Source: Intel

(intel

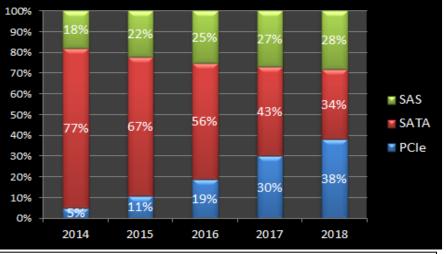
### PCI Express\* (PCIe\*) SSDs Projected to Lead in Data Center

inside" SOLID-STATE DRIVE

′intel

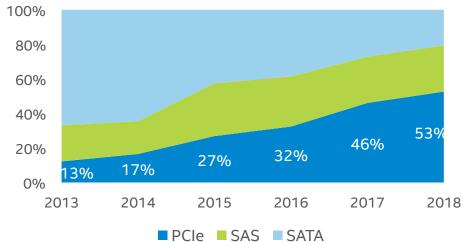
PCI Express\* (PCIe\*) projected as leading SSD interface in DC by 2018

#### Enterprise SSD by Interface



Source: International Data Corporation (IDC). Worldwide Solid State Drive 2014-2018 Forecast, Doc #248727, June 2014 PCI Express\* (PCIe\*) is projected to lead even sooner by capacity

Data Center SSD – GB by Interface

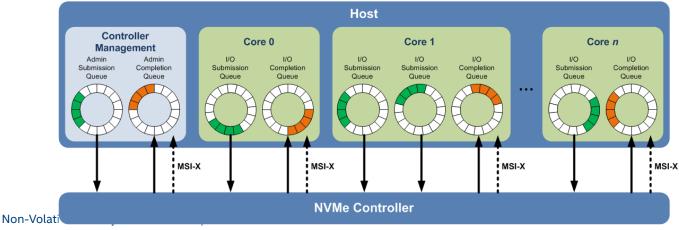


Source: Intel Market Model and multiple industry analysts

PCIe SSDs lead the way by embracing industry standards

### NVM Express\* Technical Overview

- Supports deep queues (64K commands per queue, up to 64K queues)
- Supports MSI-X and interrupt steering
- Streamlined & simple command set (13 required commands)
- Optional features to address target segment
  - Data Center: End-to-end data protection, reservations, etc.
  - Client: Autonomous power state transitions, etc.
- Designed to scale for next generation NVM, agnostic to NVM type used

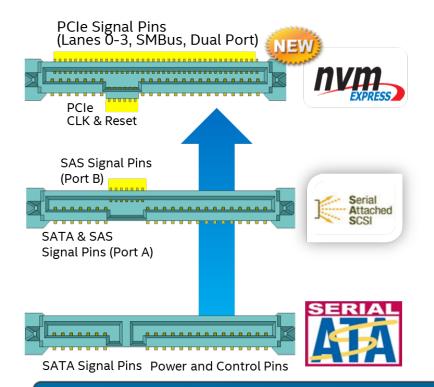






### Serviceable Form Factor for Data Center





A serviceable (hot pluggable) form factor is critical in Data Center

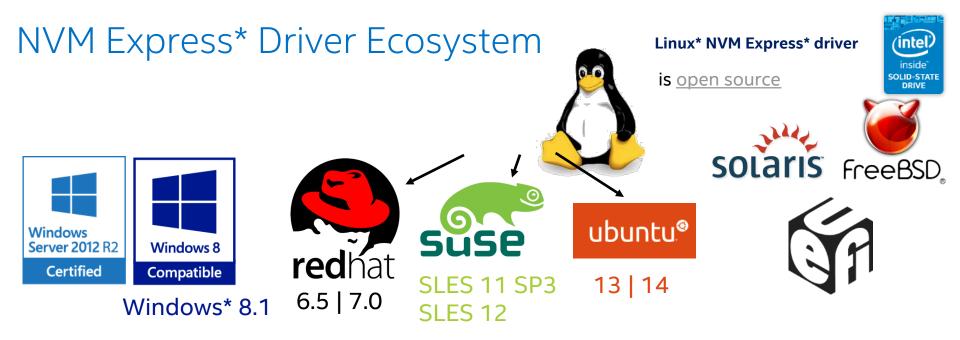
The SFF-8639\* form factor / connector supports NVM Express\* (NVMe), SAS, and SATA

Enables OEMs to transition at their own speed SFF-8639 can be used with existing platforms using a PCI Express\* (PCIe\*) adapter

#### NVMe is a great Data Center investment, near-term and long-term.

Non-Volatile Memory Solutions Group \*Other names and brands may be claimed as the property of others.









### Analyzing What Matters

- What matters in today's Data Center is not just IOPs and bandwidth
- Let's look at efficiency of the software stack, latency, and consistency

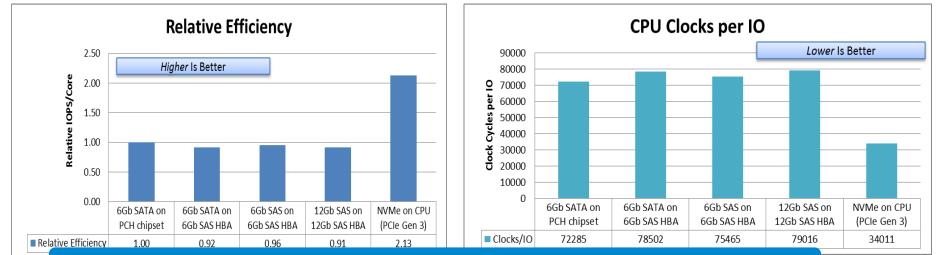
Server Setup						
<ul> <li>Basic 4U Intel<sup>®</sup> Xeon<sup>™</sup> E5 processor based server</li> <li>Out of box software setup</li> <li>Moderate workload: 8 workers, QD=4, random reads</li> </ul>						
Storage Protocols Evaluated						
Interface	6Gb SATA*	6Gb SATA	6Gb SAS	12Gb SAS	NVMe PCIe* Gen 3	
Attach Point	PCH chipset	6Gb SAS HBA	6Gb SAS HBA	12Gb SAS HBA	CPU	
Not strenuous on purpose – evaluate protocol and not the server						
Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark* and MobileMark*, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. YNon-Volatiles Memory Solutions Group rformance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product (intel)						





The Efficiency of NVM Express\* (NVMe)

- CPU cycles in a Data Center are precious
- And, each CPU cycle required for an IO adds latency
- NVM Express<sup>\*</sup> (NVMe) takes less than half the CPU cycles per IO as SAS



### With equivalent CPU cycles, NVMe delivers over 2X the IOPs of SAS!

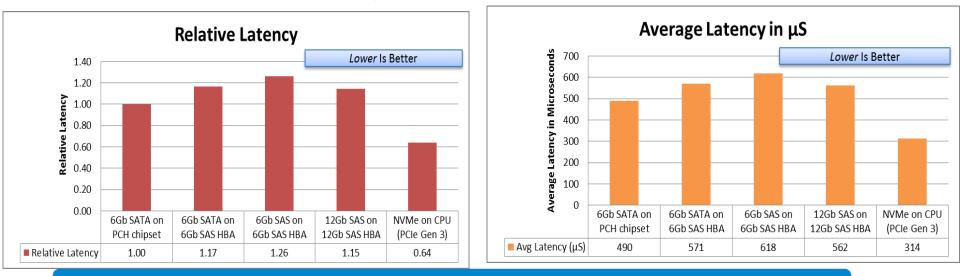
Tests document performance of components on a particular test, in specific systems. Differences in hardware, software, or configuration will affect actual performance. Consult other sources of information to evaluate performance as you consider your purchase. Test and System Configurations: PCI Express" (PCIe")/NVM Express" (NVMe) Measurements made on Intel® Core™ i7-3770S system @ 3.1GHz and 4GB Mem running Windows\* Server 2012 Standard O/S, Intel PCIe/NVMe SSDs, data collected by IOmeter\* tool, PCIe/NVMe SSD is under development. SAS Measurements from HGST Ultrastar\* SSD800M/1000M (SAS) Solid State Drive Specification. SATA Measurements from Intel® Solid State Drive Development. SATA Measurements from Intel® Solid State Drive Development.

http://www.intel.com/performance. Source: Intel Internal Testing



### The Latency of NVM Express\* (NVMe)

- The efficiency of NVM Express\* (NVMe) directly results in leadership latency
- When doubling from 6Gb to 12Gb, SAS only reduces latency by ~ 60  $\mu S$
- NVMe is more than 200 µs lower latency than 12 Gb SAS



#### NVMe delivers the lowest latency of standard storage interface

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#### http://www.intel.com/performance. Source: Intel Internal Testing

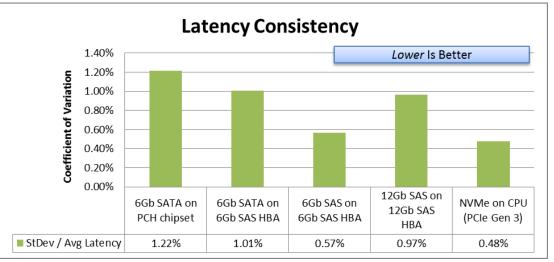


The Consistency of NVM Express\* (NVMe)

NVM Express<sup>\*</sup> (NVMe) leadership on latency and efficiency is consistently ama



• SAS is a mature software stack with over a decade of tuning, yet the first generation NVM Express software stack has 2 to 3X better consistency



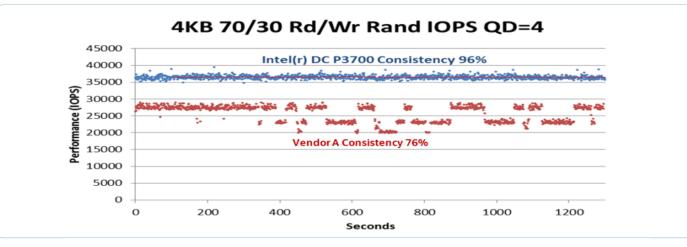
### NVMe is already best in class, with more tuning yet to come

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### Intel<sup>®</sup> SSD DC P3700 Series Consistently Amazing

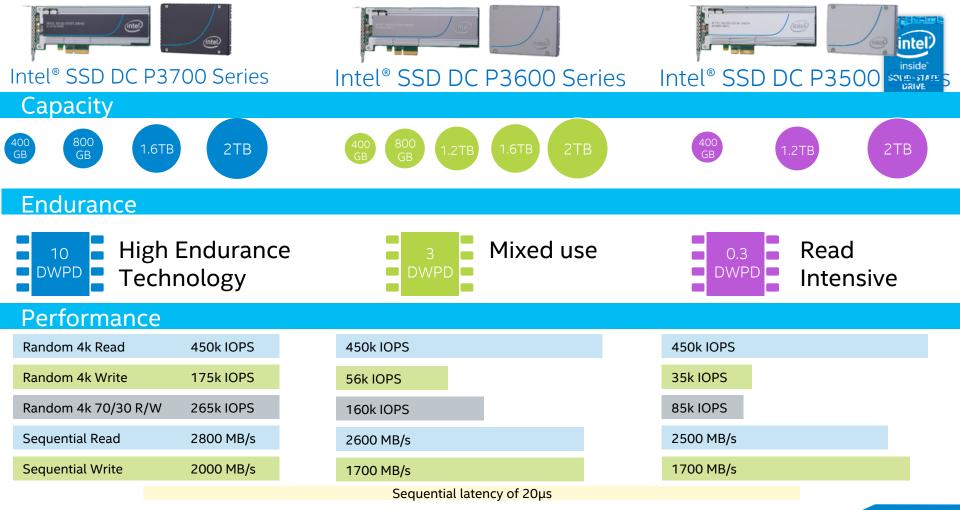




#### **Designed for Real Data Center Applications**

- ✓ High consistency enables scalable performance across RAID sets
- Right balance of read/write performance optimizes mixed workloads
- $\checkmark$  Low latency at low queue depths delivers high performance

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### Your Stuff Works Better w/ NVMe!



Private Cloud





**Big data** 

P		P
:	:	:

HPC

Virtual	lization
VIILUa	lization

P3700 P3600 P3500 P3600 P3500

Database

NVMe SSDs lower enterprise IT TCO by enabling increased Virtual Machine scalability and optimizing platform utilization

Software Defined Infrastructure or hyper convergence is made affordable with high performance SSDs

Consistent, low latency, high bandwidth performance of NVMe shines in traditional relational databases

Analytics and NoSQL databases fully utilize NVMe performance to provide near real time results

NVMe keeps up with high bandwidth demands of HPC to speed up overall workflow times by an order of magnitude





## HPC 3700 P3600 P3500

### Top PCIe SSD Use Cases



- Burst buffering to accelerate cluster I/O performance, typical rate is 30:1 (attach with Intel Ethernet products)
- IEEL (Intel Enterprise Edition for Lustre) with Intel NVMe SSDs for ZFS L2ARC.
- Temp drive for HPC and TC (Genomics, Fluid Dynamics)
- Checkpoint restart / Memory snapshot
- Memory swap

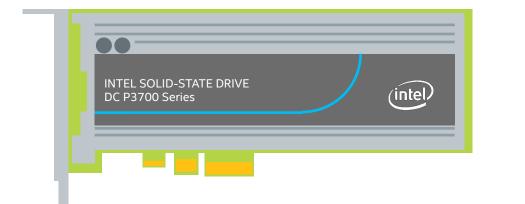
**SOURCES:** https://communities.intel.com/community/itpeernetwork/healthcare/blog/2014/11/12/sc14-accelerating-life-sciences-at-80-gbits?sr=stream&ru=99237 communities.intel.com/community/itpeernetwork/healthcare/blog/2014/11/12/sc14-accelerating-life-sciences-at-80-gbits?sr=stream&ru=99237 http://www-public.slac.stanford.edu/SciDoc/docMeta.aspx?slacPubNumber=slac-tn-15-001

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### Setup for Efficiency and Latency Analysis



- Server setup:
  - 2-Socket Intel® Xeon® E5-2690v2 + 64GB RAM + SSD Boot/Swap EPSD 4U S2600CP Family
  - Linux<sup>\*</sup> 2.6.32-461.el6.bz1091088.2.x86\_64 #1 SMP Thu May 1 17:05:30 EDT 2014 x86\_64 x86\_64 x86\_64 GNU/Linux
  - CentOS 6.5\* fresh build, yum -y update (no special kernel or driver)
- SSDs used:
  - LSI 9207-8i\* + 6Gb SAS HGST\* Drive @ 400GB & LSI 9207-8i \*+ 6Gb SATA Intel<sup>®</sup> SSD DC S3700 @ 400GB
  - LSI 9300-8i\* + 12Gb SAS HGST\* Drive @ 400GB
  - Onboard SATA Controller + SATA Intel<sup>®</sup> SSD DC S3700 @ 400GB
  - Intel<sup>®</sup> SSD DC P3700 Series NVM Express\* (NVMe) drive at 400GB
- FIO workload:
  - fio --ioengine=libaio --description=100Read100Random --iodepth=4 --rw=randread --blocksize=4096 --size=100% -runtime=600 --time\_based --numjobs=1 --name=/dev/nvme0n1 --name=/dev/nvme0n1 --name=/dev/nvme0n1 -name=/dev/nvme0n1 --name=/dev/nvme0n1 --name=/dev/nvme0n1 --name=/dev/nvme0n1 --2>&1 | tee -a NVMeONpciE.log
  - 8x workers, QD4, random read, 4k block, 100% span of target, unformatted partition

